LOGIC ELEMENT CONNECTION INFORMATION COMPILING METHOD

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DESIGN CORP

Classification:

- international:

G06F17/50; H01L21/82

- european:

Application number: JP20020171630 20020612

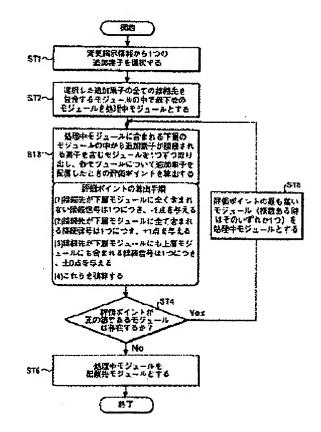
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Abstract of JP2004021315

PROBLEM TO BE SOLVED: To acquire logic element connection information in which the number of pins to be newly generated in a connection destination module is small, and alternate wiring is small.

SOLUTION: The least significant module among modules including all additional elements selected from change instruction information is defined as a module under processing, and modules including elements to which the additional elements are connected are extracted one by one from underlayer modules included in the module under processing, and evaluation points at the time of arranging the additional elements are calculated (steps ST1 to ST3). When modules whose evaluation points have positive values are present (step ST4: Yes), the module whose evaluation point is the highest(any one module when there are a plurality of such modules) is defined as the module under processing (steps ST to ST3). When only modules whose evaluation points are not more than 0 are found (a step ST4: No), the module under processing at that time is defined as an arrangement destination module (a step ST6).



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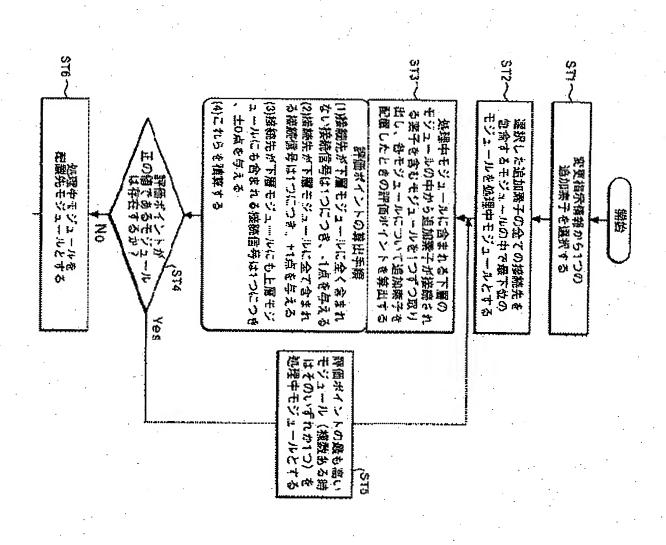
(74) Representative

CONNECTION COMPILING METHOD INFORMATION (54) LOGIC ELEMENT

(57) Abstract:

connection destination module is small, and alternate wiring is small pins to be newly generated in a acquire logic element connection information in which the number of PROBLEM TO BE SOLVED: To step ST6). an arrangement destination module (a points are not more than 0 are found only modules whose evaluation elements are calculated (steps ST1 to processing at that time is defined as (a step ST4: No), the module under processing (steps ST to ST3). When defined as the module under are a plurality of such modules) is are present (step ST4: Yes), the one by one from underlayer modules elements are connected are extracted elements to which the additional processing, and modules including defined as a module under change instruction information is additional elements selected from module among modules including al module whose evaluation point is the evaluation points have positive values processing, and evaluation points at SOLUTION: The least significant included in the module under nighest(any one module when there ST3). When modules whose he time of arranging the additional

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®Title: JP04021315A2: OVERVOLTAGE PREVENTING CIRCUIT

*Derwent Title: Overvoltage protection circuit for telephone subscriber line - has current interruption switch circuits connected respectively and turned off with overvoltage detection NoAbstract Dwg 1/4 Derwent Record

Country: JP Japan

®Kind: A

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Published / Filed: 1992-01-24 / 1990-05-14

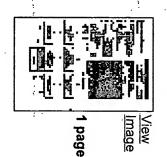
PApplication **JP1990000124403**

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insulating state obtained for the purpose of a circuit protection by providing a current breaker so balance-connected as to short-circuit to a voltage of a normal state and to be cut OFF when an overcurrent is applied, and a detector for latching the detected output of an overvoltage source cross-contact to output it.



subscriber's line 14a → a current breaker 11-1 → a receiver side → a operated by an external control input. The circuit can be protected cross-contact; and if the cross-contact is eliminated, the breakers stop supplying of a current. Thus, the breakers 11-1, 11-2 become applies an overvoltage detection signal to the current breaker to current breaker 11-2 → subscriber's line 14b, and a detector 12 source cross- contacts an overcurrent flows in a circuit of a the lines are stopped during the cross-contact, a selector 13 is currents or voltages of the lines 14a, 14b during an overvoltage insulating states of no current. The detector 12 monitors the 11-1, 11-2 are recovered to a conducting state. If the insulation of CONSTITUTION: If subscribers' lines 14a, 14b have overvoltage

by the above operation. COPYRIGHT: (C)1992,JPO&Japio

8 Family:

8 Other Abstract

None None

Info







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